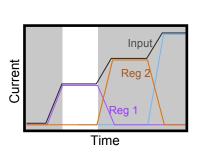
CL880x Self-Commutating Operation

Upstream Regulator Active

Rectified AC

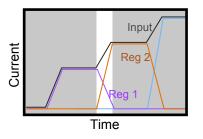
Input voltage is high enough to forward bias segment 1 but not enough to even partially bias segment 2.



Segment 1 FULLY biased Segment 2 NOT biased **N M • • • N N N N N N N N** T1> **0V** T2 = **0V** ТЗ Τ4 2V/(30Ω+15Ω+5Ω) = ð Regulator 2 ON but V_{DD} (due to Regulator 1 REGULATING NOT CONDUCTING MA **1** 2V **1** 2V **1** 2V **2**V S2 = 0.857V R_{S2} <u>S</u>4 **S**1 **S**3 R_{S1} **ω^{1š}Ω** 10Ω <u>ω^{30Ω}</u>

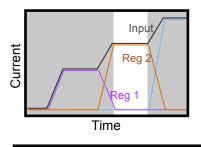
Transition

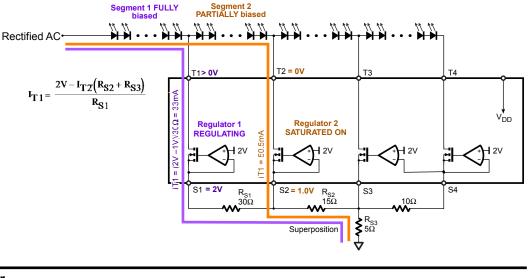
Input voltage is high enough to partially bias segment 2. Pin S2 below regulation point leaves residual voltage for S1. This allows regulator 1 to continue regulating as segment 2 comes up to speed.



Downstream Regulator Takes Over

Input voltage is high enough to fully bias segment 2. Regulator 2 is out of saturation and regulating. S2 at regulation point (2V) leaves no residual voltage for S1, choking-off regulator 1.





Segment 2 PARTIALLY biased

